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[1] System-Level Modeling and Design Methodology

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[2] Embedded Systems and Cyberphysical Systems

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[3] Embedded Systems Software

***Jian-Jia Chen** (TU Dortmund, Germany)

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Jalil Boukhobza (University of Western Brittany, France)

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Sudipta Chattopadhyay (Singapore University of Technology and Design, Singapore)

Xulong Tang (University of Pittsburg, USA)

Jeronimo Castrillon (TU Dresden, Germany)

[4] Memory Architecture and Near/In Memory Computing

***Meng-Fan Chang** (National Tsing Hua University, Taiwan)

Ken Takeuchi (Chuo University, Japan)

In-chao Lin (National Cheng Kung University Taiwan)

Deliang Fan (University of Central Florida, USA)

Guangyu Sun (Peking University, China)

Tae Hyoung (Tony) Kim (Nanyang Technology University, Singapore)
Shinichiro Shiratake (Toshiba Memory, Japan)
Dongsuk Jeon (Seoul National University, Korea)

[5] Neural Network and Neuromorphic Computing

***Deming Chen** (University of Illinois at Urbana-Champaign)
Yingyan Lin (Rice University)
Vijaykrishnan Narayanan (Penn State University)
Andrew Putnam (Microsoft)
Amir Rahmani (UC Irvine)
Theocharis Theocharides (University of Cyprus)
Yu Wang (Tsinghua University)
Grace Li Zhang (Technical University of Munich)
Shinya Takamaeda-Yamazaki (Hokkaido University)
William Hung (Cadence)
Li Jiang (Shanghai Jiao Tong University, China)

[6] Analog, RF, Mixed Signal, and Photonics

***Mark Po-Hung Lin** (National Chung Cheng University, Taiwan)
Satoshi Kawakami (Kyushu University, Japan)
Sébastien LeBeux (Lyon Institute of Nanotechnology, France)
Chien-Nan Jimmy Liu (National Chiao Tung University, Taiwan)
Soumyajit Mandal (Case Western Reserve University, USA)
Markus Olbrich (Leibniz Universität Hannover, Germany)
Jun Tao (Fudan University, China)
Lihong Zhang (Memorial University of Newfoundland, Canada)
Luca Daniel (MIT, USA)

[7] Low Power Design and Approximate Computing

***Wenjian Yu** (Tsinghua University, China)
Cheng Zhuo (Zhejiang University, China)
Hai Wang (UESTC, China)
Haibao Chen (Shanghai Jiaotong University, China)
Hao Yu (Southern University of Science and Technology, China)
Jie Han (University of Alberta, Canada)
Yuko Hara-Azumi (Tokyo Institute of Technology, Japan)
Jun Shiomi (Kyoto University, Japan)

[8] Logic/High-Level Synthesis and Optimization

***Bing Li** (Technical University of Munich, Germany)
Wei Zhang (Hong Kong University of Science and Technology, Hong Kong)
Tsung-Wei Huang (University of Illinois at Urbana-Champaign, USA)
Oliver Keszöcze (Friedrich–Alexander University Erlangen–Nürnberg, Germany)
Cunxi Yu (University of Utah, USA)
Yung-Chih Chen (Yuan Ze University Taiwan)

[9] Physical Design

***Iris Hui-Ru Jiang** (National Taiwan University, Taiwan)
Martin D. F. Wong (University of Illinois at Urbana-Champaign, USA / Chinese Univ. of Hong Kong, China)
Hung-Ming Chen (National Chiao Tung University, Taiwan)
Jianli Chen (Fuzhou University, China)
Shao-Yun Fang (National Taiwan University of Science and Technology, Taiwan)
Tung-Chieh Chen (Maxeda Technology, Taiwan)

[10] Design for Manufacturability and Reliability

***Ulf Schlichtmann** (Technical University of Munich, Germany)
Evangeline Young (Chinese University of Hong Kong, Hong Kong)
Hussam Amrouch (Karlsruhe Institute of Technology, Germany)

Yongfu Li (Shanghai Jiao Tong University, China)
Bei Yu (Chinese University of Hong Kong, Hong Kong)
Changhao Yan (Fudan University, China)

[11] Timing and Signal/Power Integrity

***Masanori Hashimoto** (Osaka University, Japan)
Yu-Guang Chen (Yuan Ze University, Taiwan)
Takashi Sato (Kyoto University, Japan)
Umamaheswara Rao Tida (North Dakota State University, USA)
Yutaka Masuda (Nagoya University, Japan)

[12] Testing, Validation, Simulation, and Verification

***Kohei Miyase** (Kyushu Institute of Technology, Japan)
Ying Zhang (Tongji University, China)
Sectal Potluri (NC State University, USA)
Jin-Fu Li (National Central University, Taiwan)
Michihiro Shintani (Nara Institute of Science and Technology (NAIST), Japan)

[13] Hardware and Embedded Security

***Gang Qu** (Univ. of Maryland)
Qiaoyan Yu (University of New Hampshire)
Sheng Wei (Rutgers University)
Jiafeng Xie (Villanova University)
Paul Ampadu (Virginia Tech.)
Sying-Jyan Wang (National Chung Hsing University)
Chiou-Yng Lee (Lunghwa University)
Hiromitsu Awano (Osaka University, Japan)
Song Bian (Kyoto University, Japan)
Jiliang ZHANG (Hunan University)
Xueyan Wang (Beihang University)
Weiqiang Liu (Nanjing University of Aeronautics and Astronautics)

[14] Emerging Technologies and Applications

***Robert Wille** (Johannes Kepler Universität Linz, Austria)
Xing Huang (National Tsing Hua University, Taiwan)
Xueqing Li (Tsinghua University, China)
Sangyoung Park (TU Berlin, Germany)
Debjyoti Bhattacharjee (Nanyang Technological University, Singapore)
Tsun-Ming Tseng (Technical University of Munich, German)
Rudy Raymond H.P. (IBM Research – Tokyo, Japan)
Jie-Hong Roland Jiang (National Taiwan University, Taiwan)
Pingqiang Zhou (ShanghaiTech University, China)